



ASA-1062

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of

T. SASAKI et al.

Serial No. 10/073,312

Group Art Unit: 2825

Filed: February 13, 2002

Examiner: M. Dimyan

For: DESIGN METHOD AND SYSTEM FOR
SEMICONDUCTOR INTEGRATED CIRCUITS

NOTICE OF APPEAL

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

February 25, 2005

Sir:

Applicants hereby appeal to the Board of Patent Appeals and Interferences from the decision dated August 25, 2004, of the Primary Examiner.

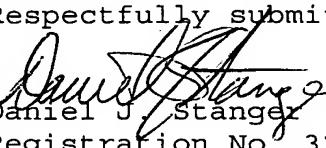
A Credit Card Payment Form in the amount of \$500.00 is enclosed for the appropriate fee.

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Respectfully submitted,


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